

**Notice of References Cited**

Application/Control No.

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Applicant(s)/Patent Under

Reexamination

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Art Unit

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Page 1 of 1

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	B	US-6,125,466	09-2000	Close et al.	714/758
	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Hsu et al., "Error Correction Capabilities of Binary Mapped Reed-Solomon Codes with Parity Bits Appended to all Symbols", IEE Proc.-Commun., Vol. 141, No. 4, August 1994, pp. 209-211.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.